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APR 0 4 2005

Applicant: Gove et al

Art Unit: 2155

Serial No.: 09/875,136

Examiner:

Filed: June 6, 2001

APR-04-2005 09:11

Docket: TI-14657A.2

For: SINGLE INTEGRATED CIRCUIT EMBODYING A DUAL HETEROGENOUS

PROCESSORS WITH SEPARATE INSTRUCTION HANDLING HARDWARE

(amended title)

AMENDMENT UNDER 37 C.F.R. 1.111

Assistant Commissioner for Patents
Washington, D.C. 20231

CERTIFICATION OF FAX TRANSMITTAL UNDER 37 C.F.R. \$1.6(b)

I hereby certify that the above correspondence is being facsimile transmitted to the Patent and Trademark Office on April 4, 2005.

Robin E. Barnum

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated January 31, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

APR-04-2005 09:10

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P.01

To:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Robert J. Gove, et al.

Serial No.:

09/875,136

Filed: For:

May 30, 2001

Reduced Area of Crossbar and Method of Operation

TI-14657A.2

Art Unit: 2183

Total Pages Sent: 19

Examiner: William M. Treat

Conf. No.: 8645

CERTIFICATION OF FACSIMILE TRANSMISSION

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Robin E. Barnum

April 4, 2005

FACSIMILE COVER SHEET

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NAME OF INVENTOR(S):		RECEIPT DATE & SERIAL NO.:
Robert J. Gove, et al.		Serial No.: 09/875,136
TITLE OF INVENTION:		Filing Date: May 30, 2001
Reduced Area of Crossbar and Method of Operation		
TI FILE NO.:	DEPOSIT ACCT. NO.:	
TI-14657A.2	20-0668	
FAXED: 4/4/05		
DUE: 4/30/05	DD144.4	1
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